Docket No.

250051US2S DIV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Mitsuhiro NOGUCHI, et al.

SERIAL NO: New Application GAU: Unassigned EXAMINER: Unassigned FILED: Herewith

FOR:

DATA WRITING METHOD FOR SEMICONDUCTOR MEMORY DEVICE AND SEMICONDUCTOR

MEMORY DEVICE

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

Applicant(s) wish to disclose the following information.

REFERENCES

The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed
references are NOT attached, because they have been previously cited in parent application no. 10/077,757, filed
02/20/2002.

☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present
application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s)
is attached along with PTO 1449.

A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

Each item of information contained in this information disclosure statement was first cited in any communication
from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of
this statement.

□ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

SPIVAK, McCLELLAND, MAIER & NEUSTADTI P.C.

Registration No. 28,870

Customer Number

Tel. (703) 413-3000 Fax. (703) 413-2220 (OSMMN 05/03)

I:\ATTY\EHK\APPLICATIONS\250051US2DIV\250051 IDS.DOC

LIST OF REFERENCES CITED BY APPLICANT	Form PTO 1449		U.S. DEPARTMENT OF COMMERCE		ATTY DOCKET NO.		SERIAL NO.						
Mitsuhiro NOGUCHI, et al. Filling DATE GROUP Unassigned	(Modified)		PATENT AND TRADEMARK OFFICE		250051US2S DIV		New Application						
Name					APPLICANT								
Herewith	LIST OF	REFE	RENCES CITED BY AP	PLICANT	Mitsuhiro NOGUCHI, et al.								
DOCUMENT DATE NAME CLASS SUB FAPROPRIATE					FILING DATE		GROUP						
DOCUMENT DATE NAME CLASS SUB FILING DATE NAME A					Herewith		Unassi	gned					
NUMBER	U.S. PATENT DOCUMENTS												
AB 6,563,746 05-2003				DATE	NAME	CLASS							
AC		AA	5,452,249	09/19/95	MIYAMOTO ET AL.								
AD 6,091,840 07-2000 KAWAHARA ET AL.		AB	6,563,746	05-2003	FUJIOKA ET AL.								
AE		AC	6,496,418	12-2002	KAWAHARA ET AL.								
AF		AD	6,091,640	07-2000	KAWAHARA ET AL.								
AG		AE	5,835,436	11-1998	OOISHI								
AH		AF											
AI		AG											
AJ		АН											
AK		Al											
AL		AJ											
AM		AK											
No		AL											
DOCUMENT DATE COUNTRY TRANSLATION YES NO		AM		 									
DOCUMENT NUMBER		AN											
DOCUMENT NUMBER			· · · · · · · · · · · · · · · · · · ·	FO	REIGN PATENT DOCUMENTS	<u> </u>							
NUMBER			DOCUMENT					TOANO	ATION				
AP 7-182886 07/21/95 JAPAN X AQ				DATE	COUNTRY								
AR AR AS AS AT AU AV OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.) When the company of the company		AO	2000-76872	03/14/00	JAPAN				Х				
AR AS AS AT AT AU AU AV AV AV AX		AP	7-182886	07/21/95	JAPAN				· X				
AS AT AU AU AV OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.) OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.) AW Jue-Hsien CHERN, et al., "Multilevel Metal Capacitance Models for CAD Design Synthesis Systems", IEEE ELECTRON DEVICE LETTERS, VOL. 13, NO. 1 January 1992, pgs: 32-34 AX AY AZ AZ Additional References sheet(s) attached Examiner Date Considered *Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in		AQ											
AT AU AV OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.) AW Jue-Hsien CHERN, et al., "Multilevel Metal Capacitance Models for CAD Design Synthesis Systems", IEEE ELECTRON DEVICE LETTERS, VOL. 13, NO. 1 January 1992, pgs: 32-34 AX AY AZ AZ Additional References sheet(s) attached Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in		AR											
AU OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.) AW Jue-Hsien CHERN, et al., "Multilevel Metal Capacitance Models for CAD Design Synthesis Systems", IEEE ELECTRON DEVICE LETTERS, VOL. 13, NO. 1 January 1992, pgs: 32-34 AX AY AZ AZ Date Considered *Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in		AS											
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.) AW Jue-Hsien CHERN, et al., "Multilevel Metal Capacitance Models for CAD Design Synthesis Systems", IEEE ELECTRON DEVICE LETTERS, VOL. 13, NO. 1 January 1992, pgs: 32-34 AX AY AZ AZ Additional References sheet(s) attached Examiner Date Considered *Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in		AT											
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.) AW Jue-Hsien CHERN, et al., "Multilevel Metal Capacitance Models for CAD Design Synthesis Systems", IEEE ELECTRON DEVICE LETTERS, VOL. 13, NO. 1 January 1992, pgs: 32-34 AX AY AZ Additional References sheet(s) attached Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in		AU											
AW Jue-Hsien CHERN, et al., "Multilevel Metal Capacitance Models for CAD Design Synthesis Systems", IEEE ELECTRON DEVICE LETTERS, VOL. 13, NO. 1 January 1992, pgs: 32-34 AX AY AZ AZ AZ Date Considered *Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in		AV											
AW DEVICE LETTERS, VOL. 13, NO. 1 January 1992, pgs: 32-34 AX AY AZ AZ AZ AZ Additional References sheet(s) attached Examiner Date Considered *Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in			OTHER RE	FERENCES (Including Author, Title, Date, Pertinen	t Pages, e	tc.)						
AX AY AZ AZ Additional References sheet(s) attached Examiner *Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in		Jue-Hsien CHERN, et al., "Multilevel Metal Capacitance Models for CAD Design Synthesis Systems", IEEE ELECTRON											
AZ Additional References sheet(s) attached Examiner *Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in		AX											
Examiner Date Considered *Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in		AY											
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in		AZ	AZ Additional References sheet(s) attached										
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in	Examiner Date Considered												
conformance and not considered. Include copy of this form with next communication to applicant.													

I:\ATTY\EHK\APPLICATIONS\250051US2DIV\250051 PTO 1449.DOC